

FIG 1

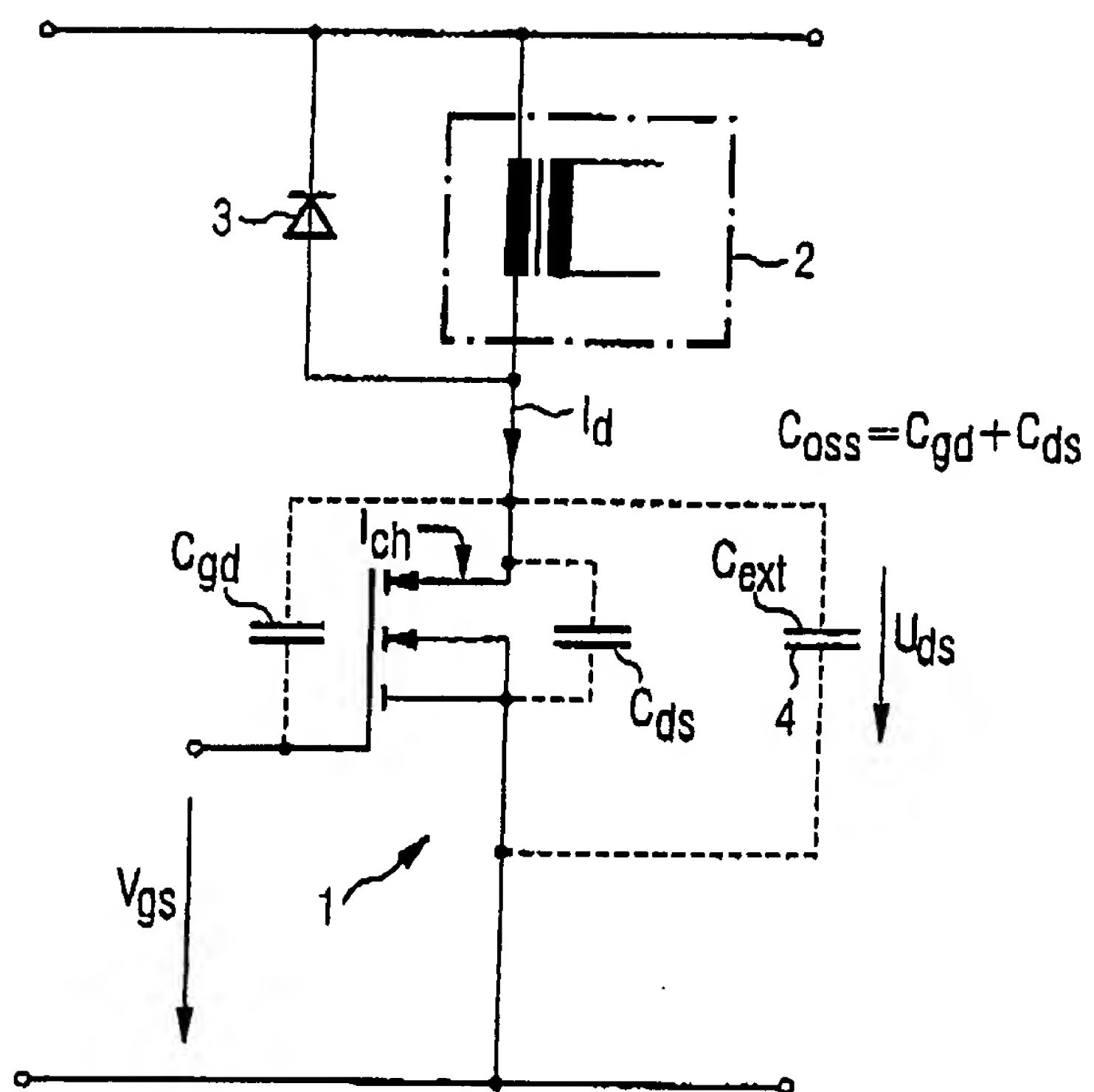


FIG 2A

PRIOR ART

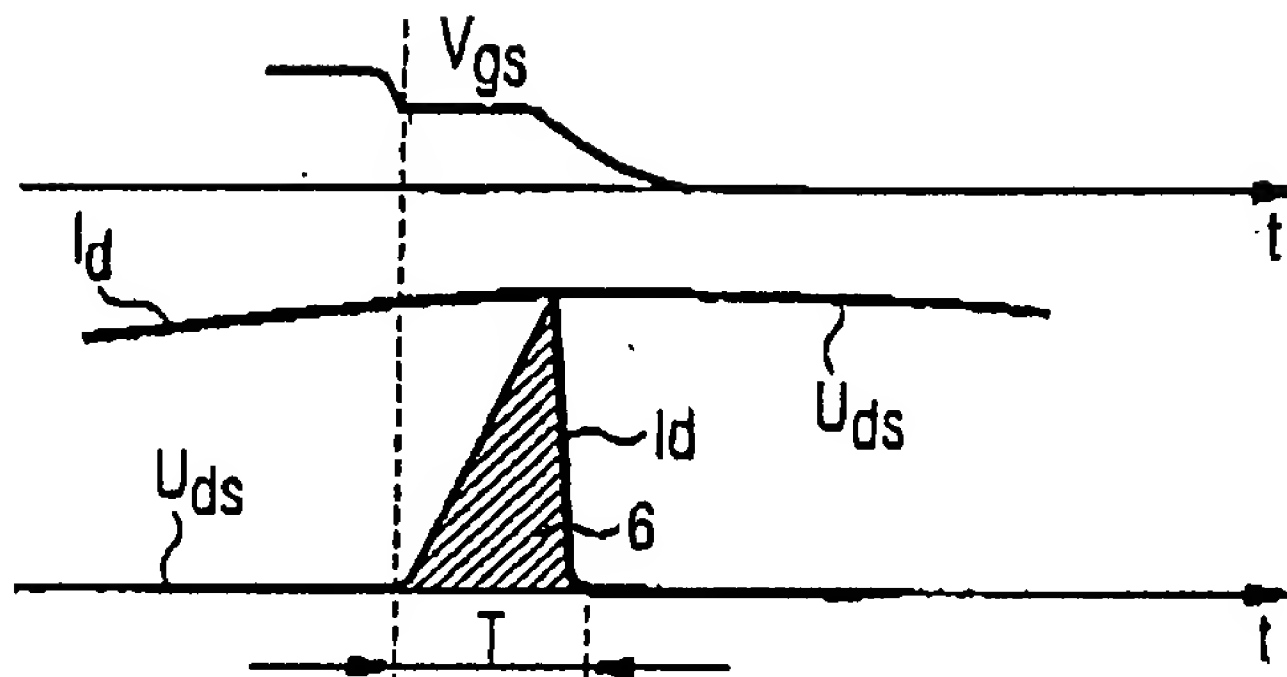


FIG 2B

PRIOR ART

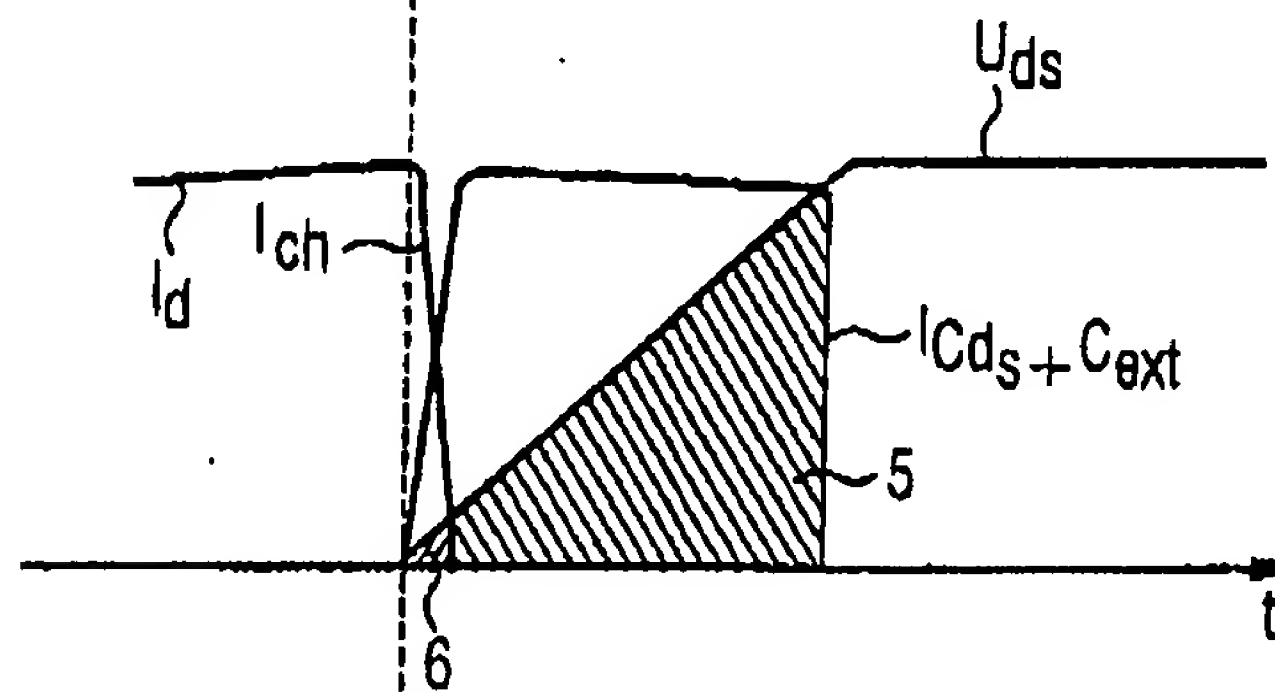


FIG 2C

INVENTION

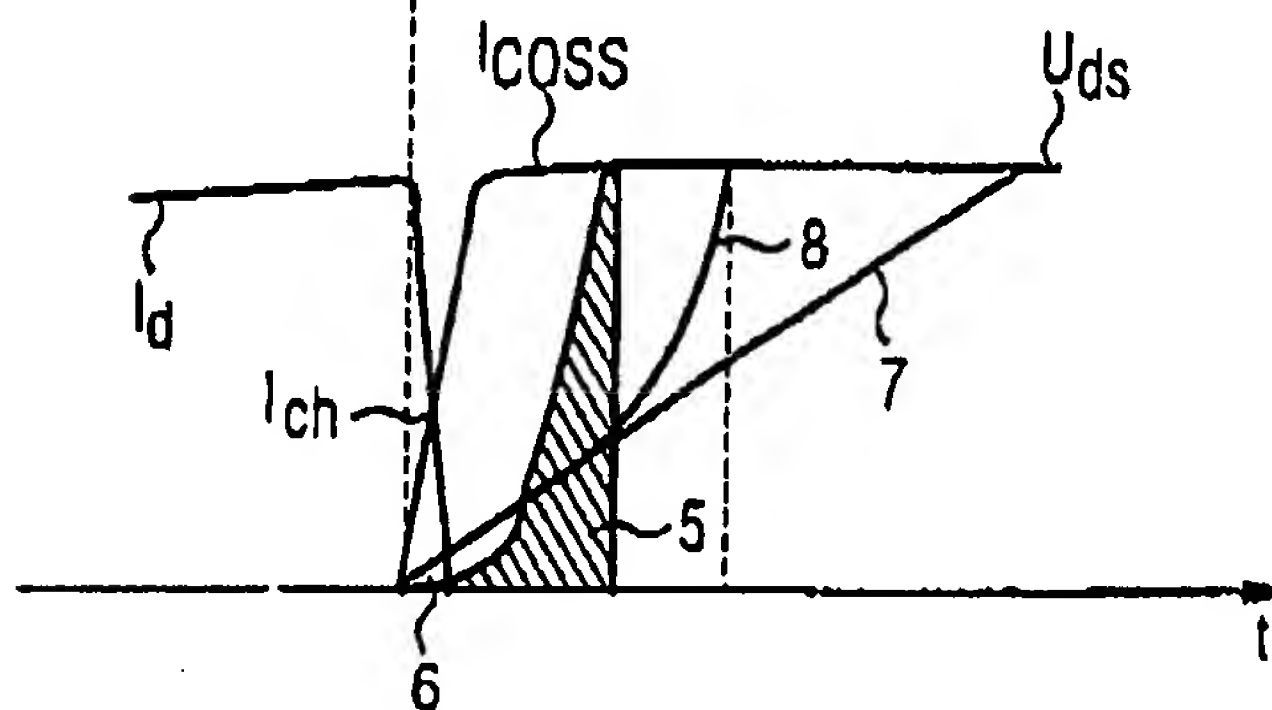


FIG 3

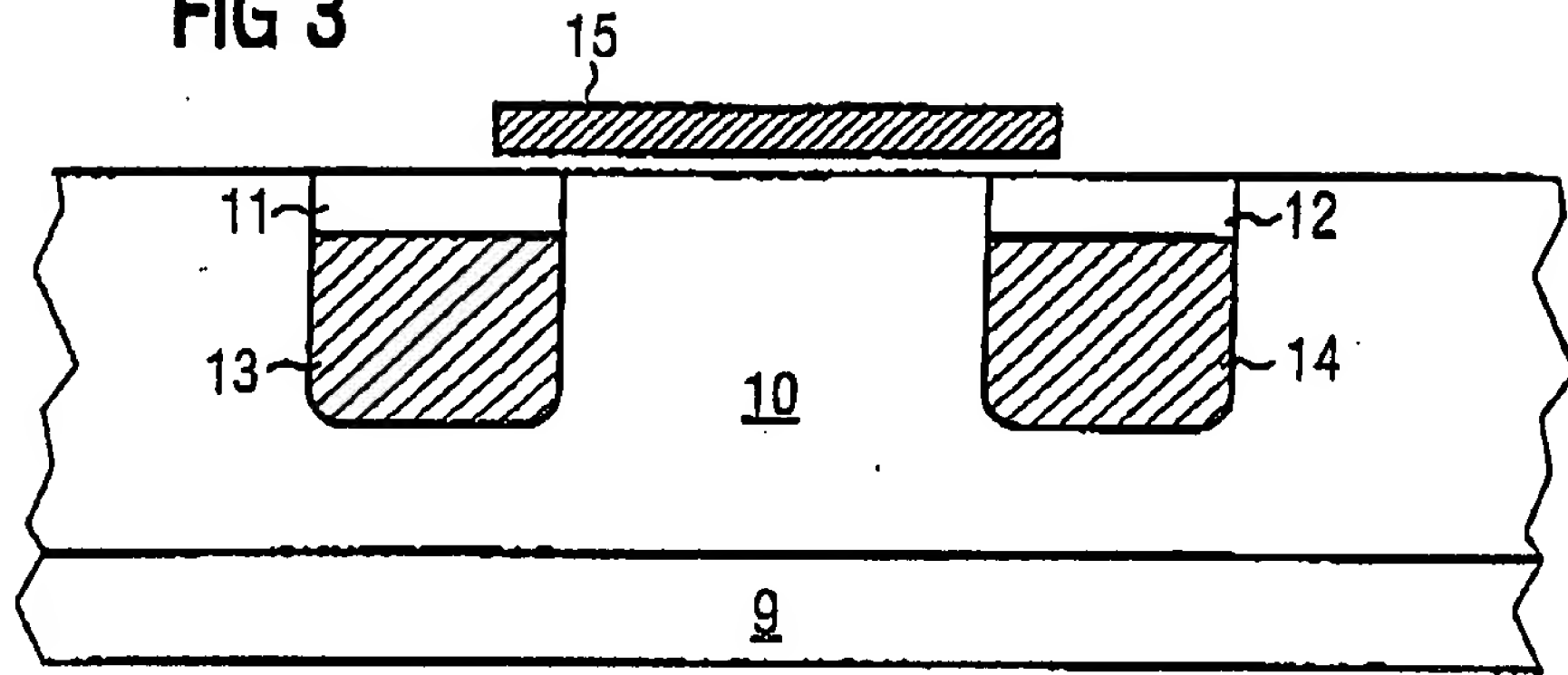


FIG 4A

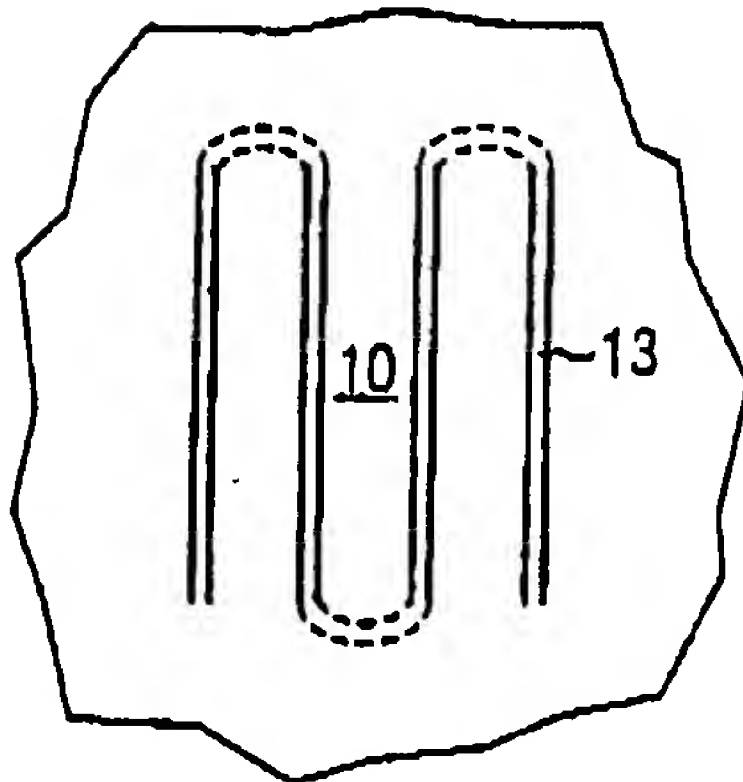


FIG 4B

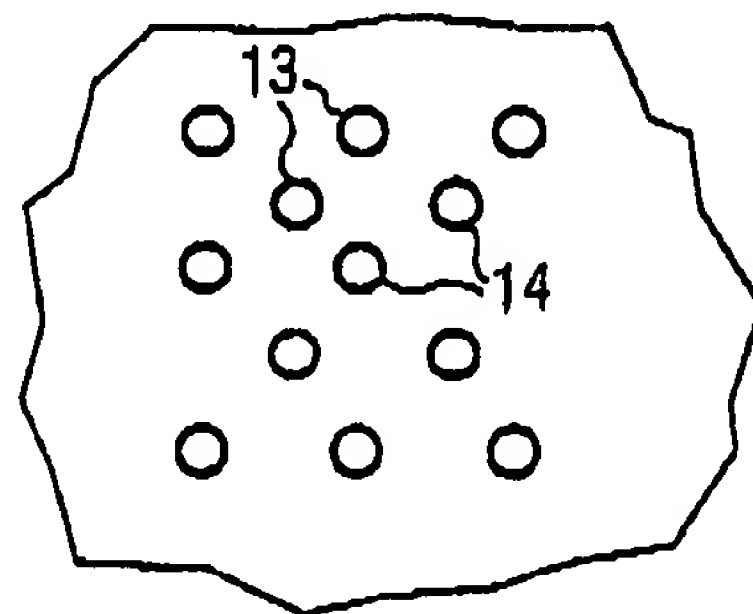


FIG 4C

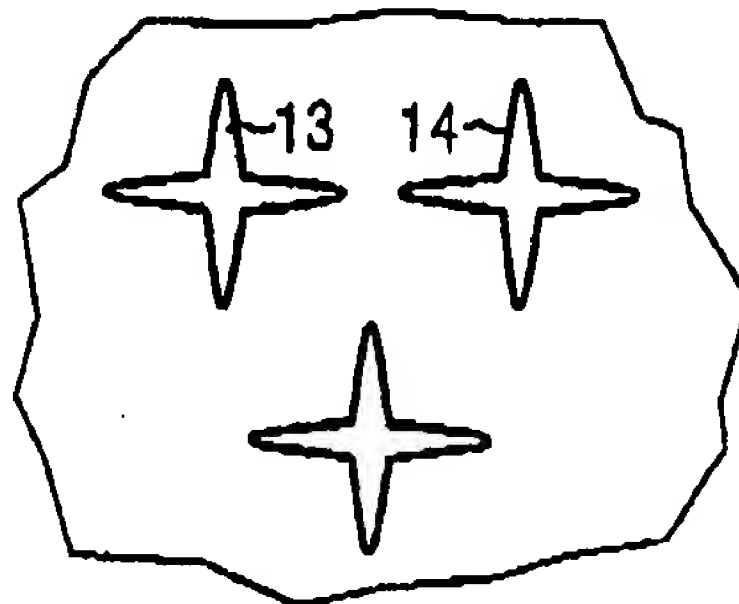
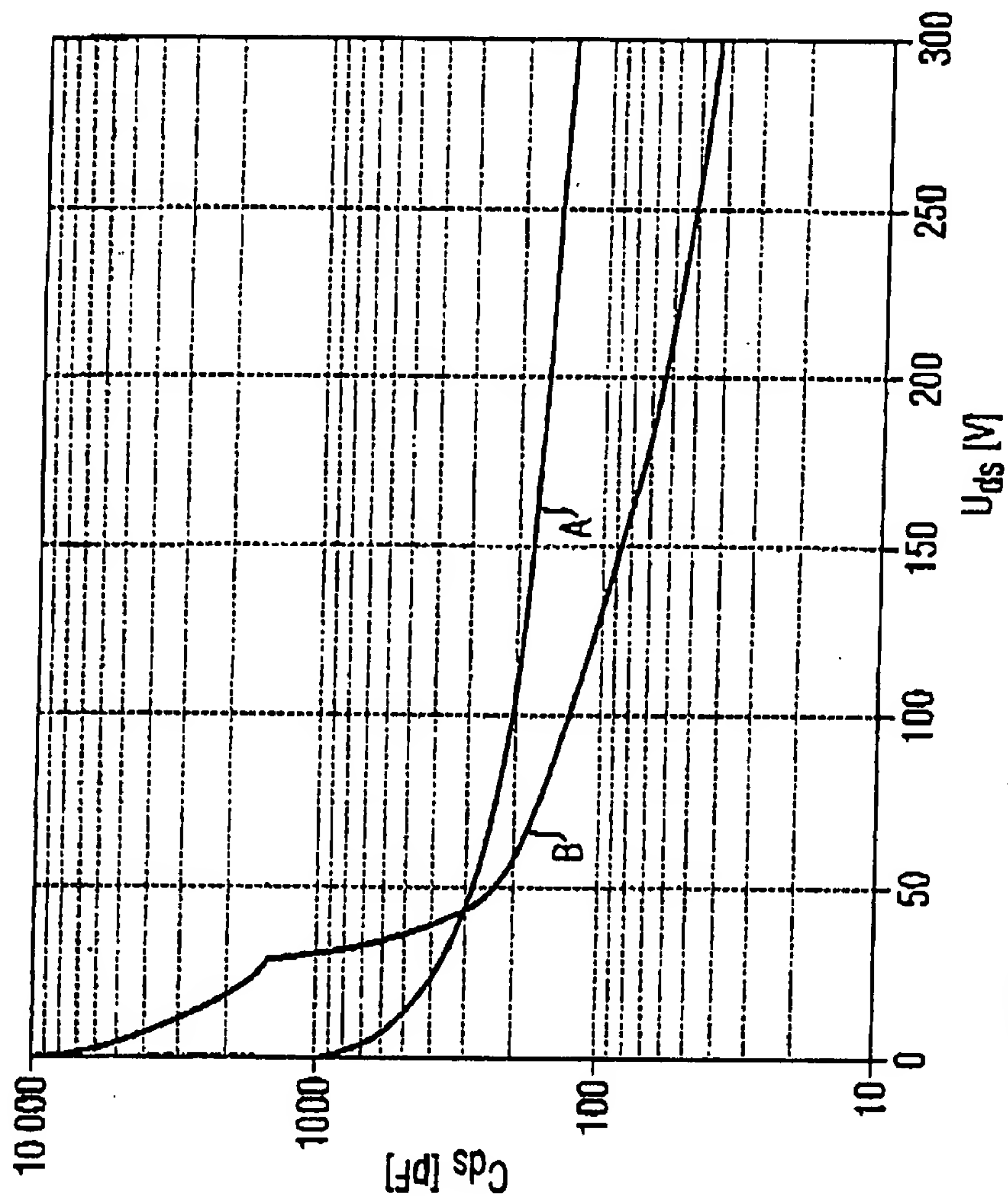


FIG 5



A: Standard-MOSFET
B: Inventive MOSFET

FIG 6A

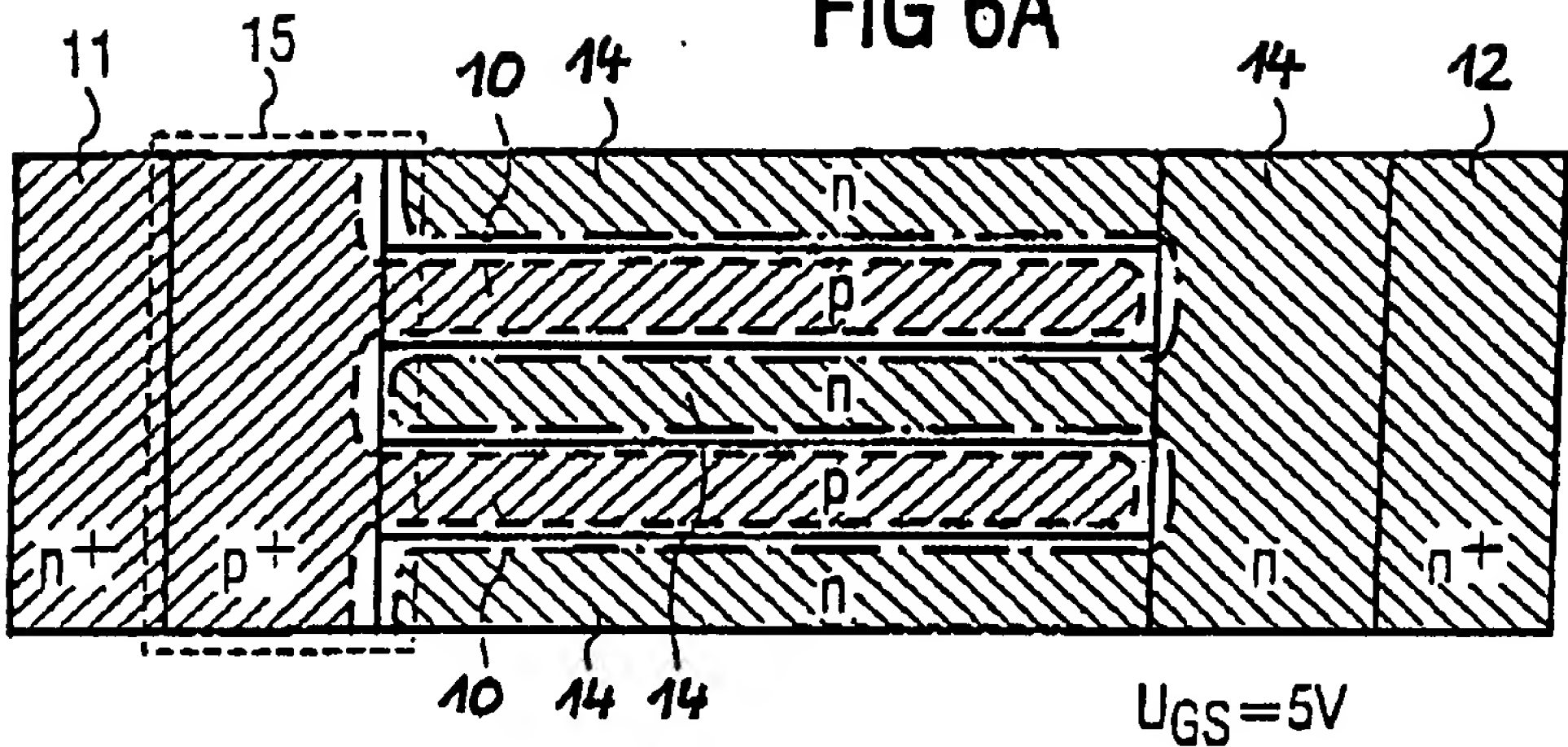


FIG 6B

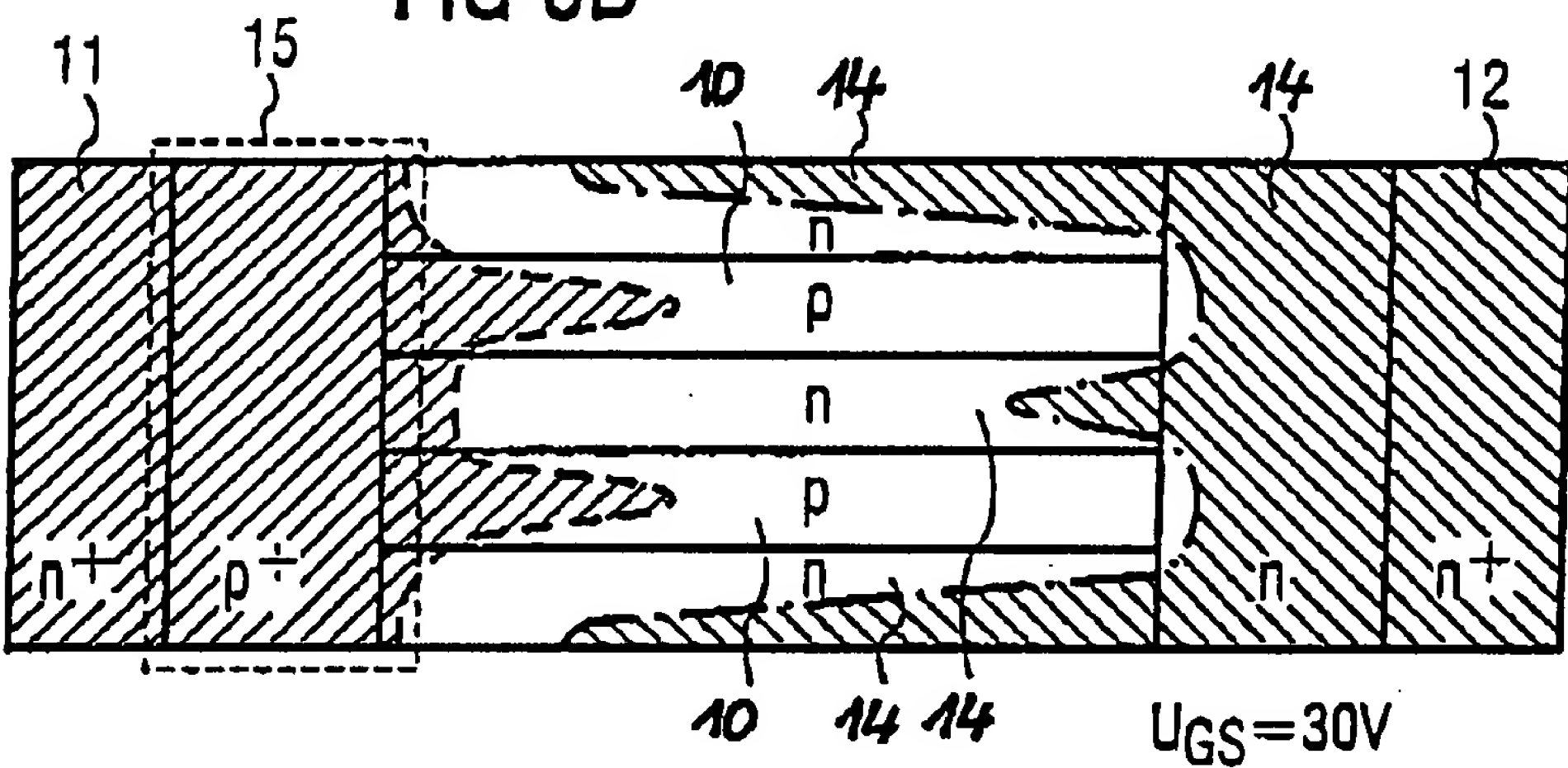


FIG 6C

